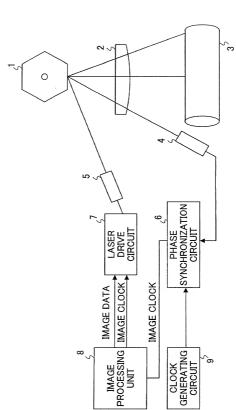


FIG.1 PRIOR ART



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FIG.2A PRIOR ART

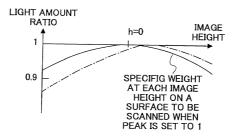
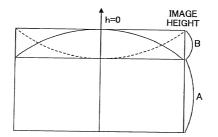
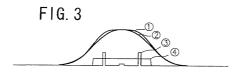


FIG.2B PRIOR ART



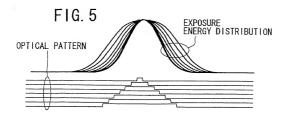


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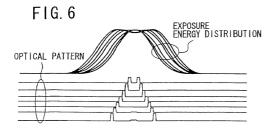
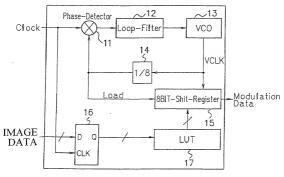


FIG. 7



Pulse-Modulation-Unit



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FIG. 8

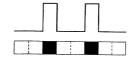
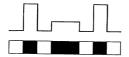
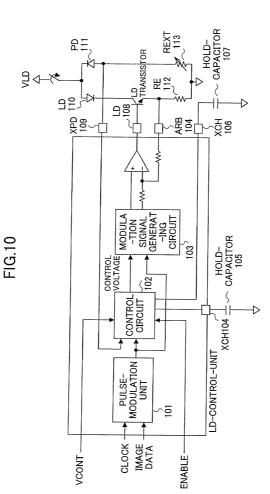
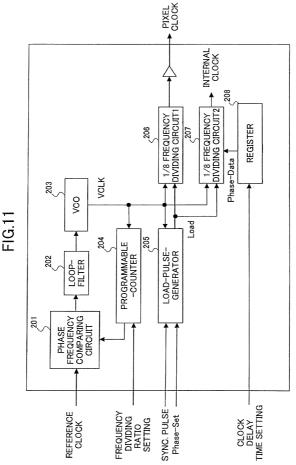
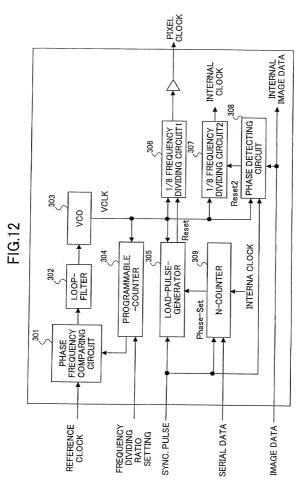


FIG. 9



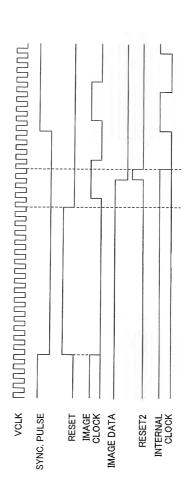






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FIG.13



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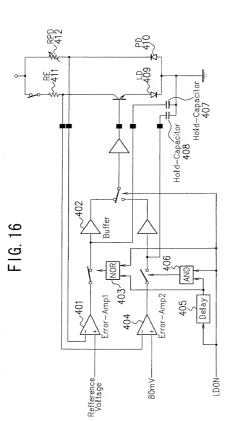
FIG 14

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PD0	0	1	0	_	0	-	0	-	PD3		0	-	0	-	0	-	0	-
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202	0				-				D2		0				-			
D0,D1 PD2 PD1 PD0	-								D2,D3 PD5 PD4 PD3		_					***		

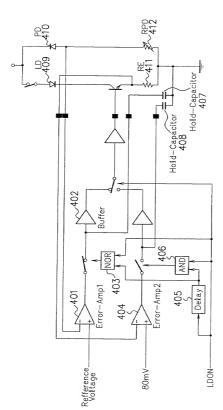
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F1G. 15







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SH Reset-XReset-XEnable—— | | | XFDON AGND-

F1G. 18

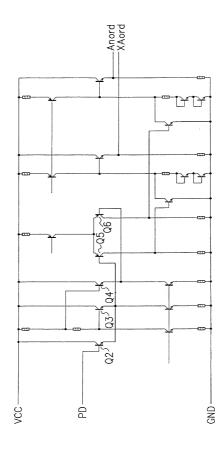


FIG. 20

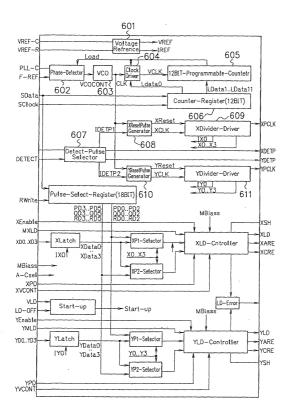


FIG. 21

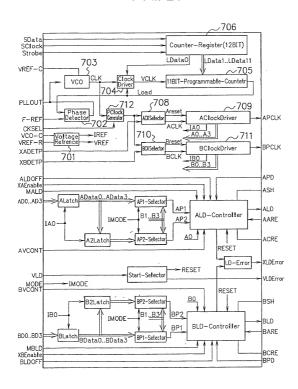


FIG.22

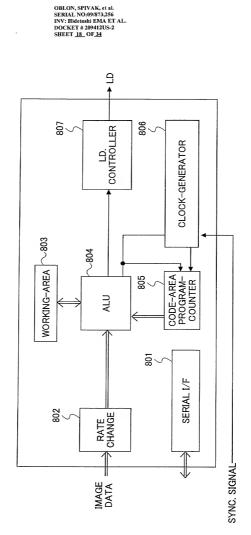
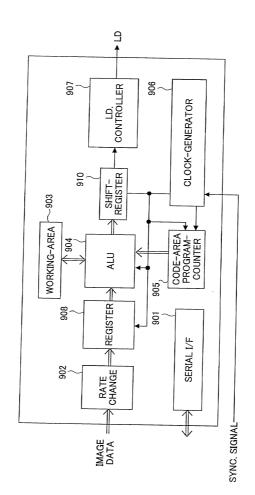
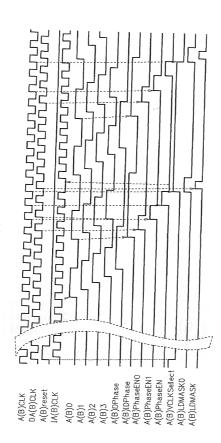


FIG.23

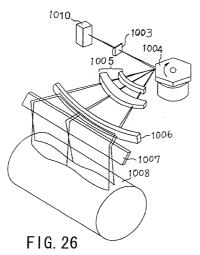


F1G. 24

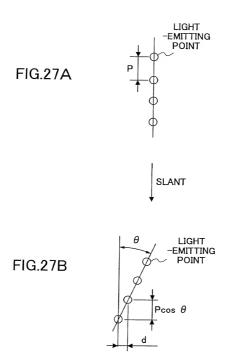


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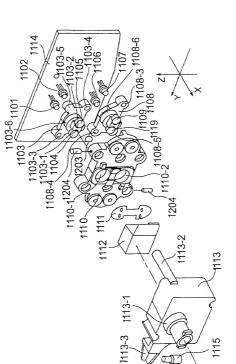
FIG. 25



1010 1015A B2 11a 1013 AX 1011 1012 1015 1015 1016

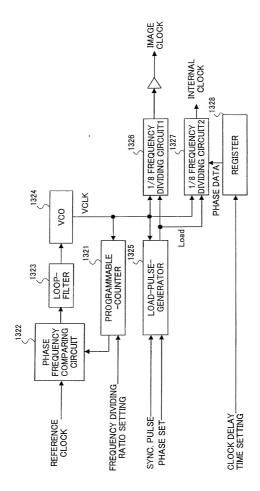


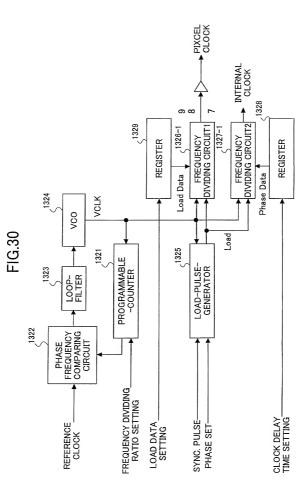
F1G. 28



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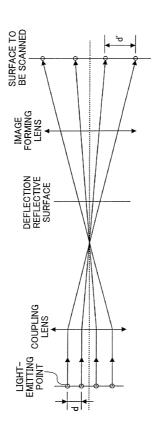
FIG.29





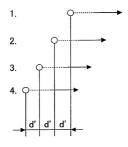
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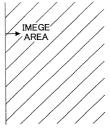
FIG.31



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FIG.32





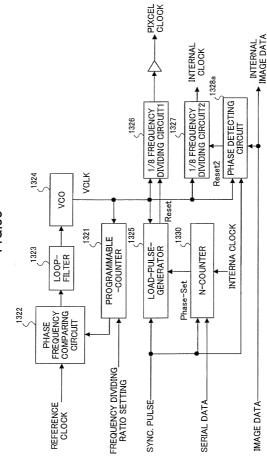
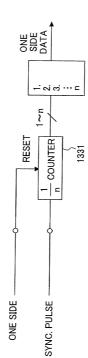
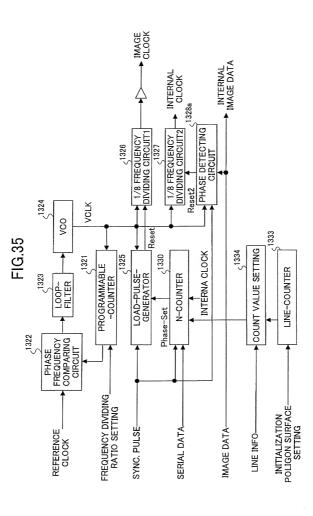


FIG.33

FIG.34

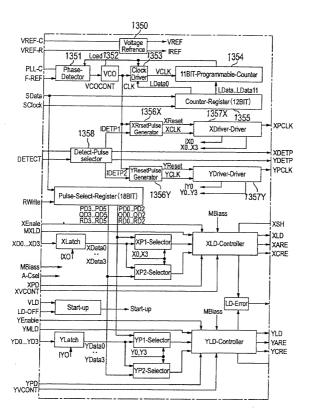
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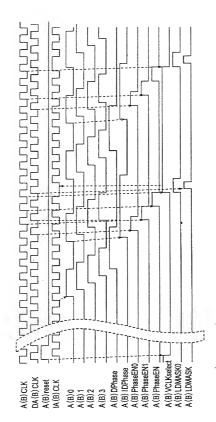




OBLON, SPIVAK, et al. SERIAL NO:09/873,256 INV: Hidetoshi EMA ET AL. DOCKET # 209412US-2 SHEET 30_OF 34

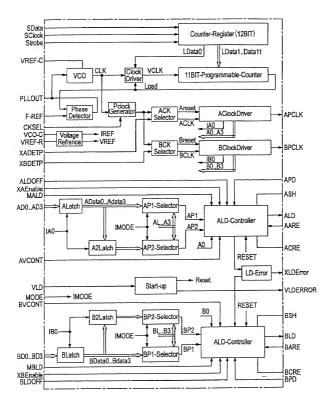
FIG. 36





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FIG. 38



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